ABSTRACT OF THE DISCLOSURE

An output port circuit is provided for a router apparatus which routes and transmits a received packet. Each flow is constituted by continuous packets and belongs to a bandwidth-guaranteed class or a

5   best-effort class. A controller controls storage and reading of a packet into and from a storage unit. Upon allocating a basic volume that is a criterion of a memory size stored in the storage unit to each flow to which the packet to be transmitted belongs, the controller manages the basic volume of each flow of the packet belonging to the bandwidth-guaranteed class by

10   individually allocating the basic volume by the memory size required by each flow, and manages the basic volume of each flow of the packets belonging to the best-effort class by collectively allocating the memory size obtained by subtracting a sum of respective basic volumes allocated to the bandwidth-guaranteed class from an entire memory size.

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